

## **HAT2035R**

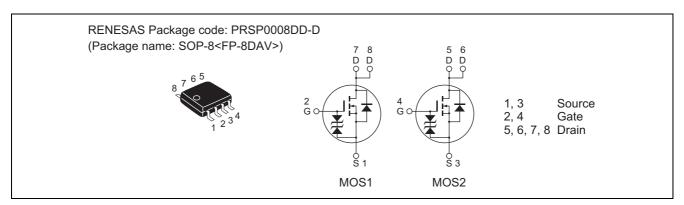
# Silicon N Channel Power MOS FET High Speed Power Switching

REJ03G1242-0100 Rev.1.00 Jun. 09, 2005

### **Features**

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to Source voltage	V <sub>DSS</sub>	150	V
Gate to Source voltage	$V_{GSS}$	±15	V
Drain current	I <sub>D</sub>	0.5	A
Drain peak current	I <sub>D(pulse)</sub> Note1	2	A
Body-Drain diode reverse Drain current	I <sub>DR</sub>	0.5	A
Channel dissipation	P <sub>ch</sub> Note2	1	W
Channel dissipation	P <sub>ch</sub> Note3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)

3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)

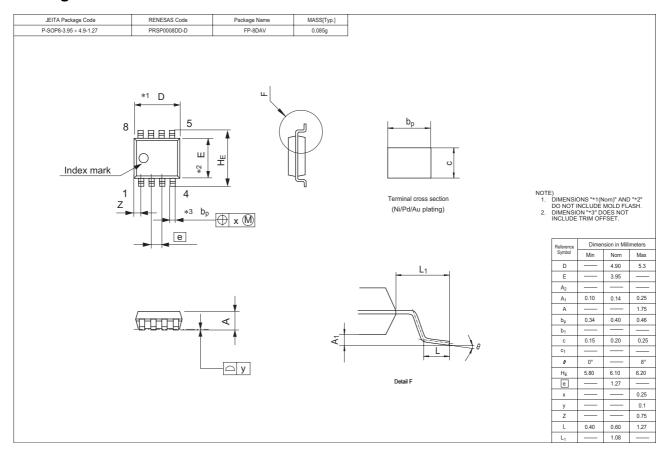
### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	150	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage	V <sub>(BR)GSS</sub>	±15	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to Source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$
Zero Gate voltage Drain current	I <sub>DSS</sub>	_	_	5	μΑ	V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0
Gate to Source cutoff voltage	V <sub>GS(off)</sub>	1.0	_	2.1	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static Drain to Source on state	R <sub>DS(on)</sub>	_	1.6	2.2	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R <sub>DS(on)</sub>	_	1.9	2.7	Ω	$I_D = 0.5 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$
	R <sub>DS(on)</sub>	_	2.4	5.5	Ω	$I_D = 2 A$ , $V_{GS} = 5 V^{Note4}$
Forward transfer admittance	y <sub>fs</sub>	0.56	0.86	_	S	$I_D = 0.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	95	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	42	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	11	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>	_	9	_	ns	$V_{GS} = 5 \text{ V}, I_D = 0.5 \text{ A},$
Rise time	t <sub>r</sub>	_	16	_	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	t <sub>d(off)</sub>	_	18	_	ns	
Fall time	t <sub>f</sub>	_	14	_	ns	]
Body-Drain diode forward voltage	$V_{DF}$	_	0.9	1.4	V	IF = 0.5 A, V <sub>GS</sub> = 0 Note4
Body-Drain diode reverse	t <sub>rr</sub>	_	90	_	ns	IF = 0.5 A, V <sub>GS</sub> = 0
recovery time						$diF/dt = 50 A/\mu s$

Notes: 4. Pulse test

### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2035R-EL-E	2500 pcs.	Taping

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